

IEI Technology Corp.



A SDK software development kit for the HDC-502E Series

User Manual



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Revision

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Driver and SDK Installation



1.1 Overview

A CD is shipped with the video capture card. The CD contains a driver for the video capture controllers on the card. When the video capture card is installed on the system, the driver must be installed. Failure to install the driver means that that video capture card cannot be detected by the system.

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The Found New Hardware Wizard will automatically start when the system detects the video capture card. Click **Cancel** to exit the wizard and follow the steps described in this chapter to install the driver and the HDCapture SDK.

1.2 Driver Installation

To install the HDC-502E driver, please follow the steps below: If the HDC-502E driver is already installed, please refer to **Section 1.2.2** to uninstall the driver first.



If the **User Access Control** dialog box appears during installation, click **Yes** to continue.

Step 1: Make sure to log in the system as the administrator.

- Step 2: Insert the driver CD.
- Step 3: Locate the "Driverinstaller.bat" file in the driver CD. Double click it. The console window appears and starts to install all drivers.
- Step 4: The screen in Figure 1-1 appears. Click Install.



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HDC-502E SDK (Windows)



Figure 1-1: Windows Security

Step 5: If the following window appears, click Install this driver software anyway.



Figure 1-2: Windows Warning Window

Step 6: The Device Driver Installation Wizard appears. Click Next to start.

Device Driver Installation Wizard	d
	Welcome to the Device Driver Installation Wizard! This wizard helps you install the software drivers that some computers devices need in order to work.
	< Back Next > Cancel

Figure 1-3: Device Driver Installation Wizard



Step 7: The video capture card driver starts to install and the screen in Figure 1-4

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appears.

Device Driver Installation Wizard
The drivers are now installing
A Please wait while the drivers install. This may take some time to complete.
< <u>B</u> ack Next > Cancel

Figure 1-4: Driver Installing

Step 8: When the driver installation is complete, the screen in Figure 1-5 appears. Click

Finish to exit.

Device Driver Installation Wizard				
	Completing the Device Driver Installation Wizard			
	The drivers were successfully in	stalled on this computer.		
	Driver Name	Status		
	✓ Fujitsu MB86H55-REB P	Device Updated		
< Back Finish Cancel				

Figure 1-5: Driver Installation Complete

Step 9: Check the device manager in the Windows control panel to ensure the driver (MB86H55-REB PCI, HDC controller and WinDriver) has been properly installed.See Figure 1-6 for the details.



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HDC-502E SDK (Windows)



Figure 1-6: Device Manager

1.2.1 Driver Installation in 64-bit Windows 7 OS

To install the driver in a 64-bit Windows 7 operating system, please do the followings.

- **Step 1:** Make sure to log in the system as the administrator.
- Step 2: Insert the driver CD.
- Step 3: Run a Command Prompt as an administrator (right click the Command Prompt and select Run as administrator).

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Step 4: In the Command Prompt window, specify the 64-bit driver directory. Then, typeDriverInstaller.bat to start the driver installation.

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Figure 1-7: Command Prompt – Driver Installation

- Step 5: Follow Step 5 ~ Step 8 in Section 1.2 to complete installing the driver to a 64-bitWindows 7 operating system.
- Step 6: Check the device manager in the Windows control panel to ensure the driver (MB86H55-REB PCI, DEVICE and WinDriver) has been properly installed. See Figure 1-8 for the details.



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HDC-502E SDK (Windows)



Figure 1-8: Device Manager – 64-bit OS

1.2.2 Uninstall Driver

To uninstall the driver, please follow the steps below.

- **Step 1:** Make sure to login the system as the administrator.
- **Step 2:** Locate the "Driveruninstaller.bat" file in the driver CD. Double click it to uninstall the driver.
- Step 3: The console window pop-up and all drivers will be uninstalled.



1.3 Software Installation

The HDC-502E comes with a video capture application – HDCapture SDK. This section describes how to install the application in Windows environment.

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1.3.1 System Requirements

The supported OS versions are listed below:

- Microsoft Windows XP SP2 32-bit
- Microsoft Windows 7 32-bit
- Microsoft Windows 7 64-bit

After installing the driver, the following programs must be installed in order to use the HDCapture SDK:

- Microsoft .NET Framework 3.0/3.5/4.0
- Microsoft DirectX 9.0c
- Win7DSFilterTweaker tool (for Windows 7 OS only)
- Visual C++ 2005 & 2008 Redistributable

Please download the setup files of these programs from the official websites and install these programs in the system. For detailed setup procedures for some of the above programs, please refer to **Appendix** Error! Reference source not found..



For the 64-bit Windows 7 operating system, the Microsoft .NET Framework 4.0 must be installed.





1.3.2 HDCapture SDK Installation

To install the HDCapture SDK, please follow the steps below.



If the **User Access Control** dialog box appears during installation, click **Yes** to continue.

- **Step 1:** Insert the driver CD.
- Step 2: Locate the HDCapture_x86_Vxxxx.msi file in the driver CD. Double click the setup file to start the installation. The user can also download the latest setup file from IEI website.
- Step 3: The HDCapture SDK Setup Wizard welcome window appears. Click Next to start.



Figure 1-9: HDCapture SDK Setup Wizard

Step 4: Select a folder for HDCapture SDK installation in Figure 1-10. Click Next to

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continue.

谩 HDCapture SDK V2.00	
Select Installation Folder	
The installer will install HDCapture SDK V2.00 to the following folder.	
To install in this folder, click "Next". To install to a different folder, enter it be	ow or click "Browse".
Eolder:	
C:\Program Files\HDCapture SDK V2.00\	B <u>r</u> owse
	Disk Cost
Cancel < <u>B</u> ack	Next >

Figure 1-10: Select Installation Folder



Step 5: The following screen appears. Click Next to confirm the installation.

Figure 1-11: Confirm Installation

Step 6: The system starts installing the HDCapture SDK.





Step 7: When the HDCapture SDK is successfully installed, the following window

appears. Click **Close** to exit.

j블 HDCapture SDK V2.00	_ _ x
Installation Complete	
HDCapture SDK V2.00 has been successfully installed.	
LICK LIUSE (O'EXIL	
Prease use windows update to check for any critical updates to the .NET Fram	ework.
Cancel < <u>B</u> ack	<u>C</u> lose

Figure 1-12: Installation Complete

1.3.3 Uninstall HDCapture SDK

To uninstall the HDCapture SDK, follow the steps below.

- **Step 1:** Select **Control Panel** \rightarrow **Programs** \rightarrow **Programs and Features**.
- Step 2: Select HDCapture SDK and click the Uninstall button to uninstall the HDCapture SDK (Figure 1-13).



Control Panel 🕨	Programs Programs and Features			
Control Panel Home	Uninstall or change a program			
View installed updates	To uninstall a program, select it from the list and then click Uninstall, Change, or Repair,			
Turn Windows features on or	······································			
off	Organize 🔻 Uninstall Change Repair			
	Name			
	Asmedia ASM104x USB 3.0 Host Controller Driver			
	ATI Catalyst Install Manager			
	HDCapture SDK V2.00			
	JMicron JMB36X Driver			
	蒙 LEADTOOLS Multimedia EVAL 17.5			
	Microsoft Visual C++ 2005 Redistributable			
	Microsoft Visual C++ 2008 Redistributable - x86 9.0.21022			
	Microsoft Visual C++ 2008 Redistributable - x86 9.0.30729.4148			
	Microsoft Visual C++ 2010 x86 Redistributable - 10.0.30319			
	VIDIA 3D Vision Controller Driver 267.85			
	NVIDIA 3D Vision Driver 267.85			
	NVIDIA Graphics Driver 267.85			
	NVIDIA HD Audio Driver 1.2.22.1			

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Figure 1-13: Uninstall HDCapture SDK







HDCapture SDK



2.1 HDCapture SDK Overview

The HDCapture SDK is a video capture tool that allows user to capture video through the SDI input ports in Windows environment.

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If you cannot open the HDCapture SDK in the 64-bit Windows 7 operating system, right-click the HDCapture SDK from the root installed directory, and click **Run as administrator**.

2.2 Video Configuration

To configure the HDCapture SDK, follow the steps below. If the older version of the HDCapture SDK is already installed, please refer to **Section 1.3.3** to uninstall it.

Step 1: Launch the HDCapture SDK. The best resolution to view HDCapture SDK is 1280 x 1024 or above.

😫 HDCapture V2.00				- • ×
Device Setting				
Device Info.	Video Source	Setting	Operation	
PCI bus 9, device 0, function 0	0 👻	Device 0	Start Stop Rebo	ot
PCI bus 9, device 1, function 0	1 -	Device 1	Start Stop Rebo	ot
				Clear





Step 2: Enable and configure the device settings by clicking the Device # (0, 1) buttons.The device number is decided by which port the device is installed.

Video Source	Setting	
0 -	Device 0	
1 -	Device 1	
	Video Source	Video Source Setting 0 • 1 • Device 1

Figure 2-2: HDC-502E Device Ports

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- Step 3: Click the Device # button. The Encoding window appears (Figure 2-3). Choose the video input format which depends on the video device. The available options include:
 - 1920x1080 (60p) (6000kps 20000kps)
 - 1920x1080 (59.94p) (6000kps 20000kps)
 - 1920x1080 (50p) (6000kps 20000kps)
 - 1920x1080 (60i) (6000kps 20000kps)
 - 1920x1080 (59.94i) (6000kps 20000kps)
 - 1920x1080 (50i) (6000kps 20000kps)
 - 1440x1080 (60i) (5000kps 20000kps)
 - 1440x1080 (59.94i) (5000kps 20000kps)
 - 1440x1080 (50i) (5000kps 20000kps)
 - 1280x720 (60p) (4000kps 20000kps)
 - 1280x720 (59.94p) (4000kps 20000kps)
 - 1280x720 (50p) (4000kps 20000kps)
 - 720x480 (60i) (2000kps 10000kps)
 - 720x480 (59.94i) (2000kps 10000kps)
 - 720x480 (50i) (2000kps 10000kps)

Step 4: Configure the encoding settings, including encoding file directory (click Ref button to choose the directory), rate control (CBR or VBR) and video encoding

bitrate (must be in the range of video format). When "CBR" is selected, the "Bitrate" text box is displayed. When "VBR" is selected, the "Average bitrate" and "Peak bitrate" text boxes are displayed. Close the window to save the settings.

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See Device 0	×	
8051 Version: 8051A100		
CPLD Version: 502EC100		
FPGA Version: 502EF113		
Encoding		
input Video format (1920×1080(59.34p) (\$000kbps 30000kbps) -		/ideo Input Format
Output		
Route h264(0).mpg Ref	V	Where to Save the
Video	C	Dutput File
Rate control Bitrate [kbps]	V	/ideo Output
1000 -	E	Bitrate (must be in
	t	he range of video
		inputionnat
	F	Rate Control
	-(CBR or VBR)

Figure 2-3: Encoding Settings

Step 5: Repeat Step 2 ~ Step 4 to configure the connected input device.





2.3 Video Capture

To use the HDCapture SDK to capture video, follow the steps below.

Step 1: Click Start to start capturing video (Figure 2-4).

See HDCapture V2.00				_ D X
Device Setting				
Device Info.	Video Source	Setting	Operation	
PCI bus 9, device 0, function 0	0 -	Device 0	Start Stop	Reboot
PCI bus 9, device 1, function 0	1 -	Device 1	Start Stop	Reboot
				Clear



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Step 2: Click Stop to stop capture.





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API Introduction





3.1 Build Environment

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The API build environment requirements are listed below. If build environment is not Microsoft Visual Studio 2005 SP1 or latter, you need to install Microsoft Visual C++ 2005 SP1 Redistributable Package (x86).

- Microsoft Windows XP SP2 32-bit
- Microsoft Windows 7 32-bit/64-bit
- DirectX SDK August 2007
- Windows SDK for Windows Vista (6.0.6000)
- Microsoft .NET Framework 2.0/3.0/3.5/4.0 32-bit/64-bit
- Microsoft Visual Studio 2005 SP1



The DumpFile.dll and PushFileSource2.dll are filters of DirectShow.

You must register them before using them. Otherwise, you will get an error.

3.2 API Introduction



If API usage in document is different from API usage in SDK source code, the API usage in SDK source code is CORRECT.



3.2.1 DeviceMan API Introduction

There are one enum, one structure and two functions in DeviceMan.dll. The source codes are listed below for reference.

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```
typedef struct _CardList_T
```

{

// Card category.

int iCategory;

// UI No, usually is the slot No.

int iUINo;

// Bus No.

int iBusNo;

// Device number.

int iDeviceNum;

// Transmitter number.

int iTransmitterNum;

// Device No of each device.

int iDeviceNo[4];

// Device information of each device.

char cDeviceInfo[4 * MAX_BUFFER_SIZE];

// Transmitter information of each device.

char cTransmitterInfo[4 * MAX_BUFFER_SIZE];

} CardList_T;

and the MAX_BUFFER_SIZE is 512.

enum

{

DEVICE_MAN_RESULT_SUCCESS = 0, DEVICE_MAN_RESULT_NULL_ADDRESS, // ASCII to Unicode failed. DEVICE_MAN_RESULT_ATOU_FAILED,



// Unicode to ASCII failed. DEVICE_MAN_RESULT_UTOA_FAILED, DEVICE_MAN_RESULT_INVALID_HANDLE, DEVICE_MAN_RESULT_BUF_ERR_MAXIMUM, DEVICE_MAN_RESULT_BUF_ERR_LENGTH, DEVICE_MAN_RESULT_BUF_ERR_OVER_MAX, // Input parameter error. DEVICE_MAN_RESULT_PARAMETER_ERROR, // Memory allocate failed. DEVICE_MAN_RESULT_MEM_ALLOC_FAILED, // No capture card. DEVICE_MAN_RESULT_NO_CARD, // Get UI No. failed. DEVICE_MAN_RESULT_GET_UI_NO_FAILED, // Get bus No. failed. DEVICE_MAN_RESULT_GET_BUS_NO_FAILED, // Get information failed. DEVICE_MAN_RESULT_GET_INFO_FAILED, // CPLD check failed. DEVICE_MAN_RESULT_CPLD_FAILED, DEVICE_MAN_RESULT_UNKNOWN_ERROR

};

DeviceManGetVersion(int* ot_ipVerYear,int* ot_ipVerMonth,int* ot_ipVerDay)
 Description: Get DeviceMan.dll verion.

Parameter:

ot_ipVerYear: Integer pointer of year version.

ot_ipVerMonth: Integer pointer of month version.

ot_ipVerDay: Integer pointer of day version.

Return:

An integer, see enum type.

DeviceManGetCardList(int* ot_ipCardNum, void** ot_ppCardList)
 Description:

Get capture card list.

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Parameter:

ot_ipCardNum: Integer pointer of card number.

ot_ppCardList: Void pointer of card list.

Return:

An integer, see enum type.

3.2.2 CPLDMan API Introduction

The CPLDMan.dll is the same with the DeviceMan.dll. The detail usage can be found in the source code.

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enum

{

CPLD_RESULT_SUCCESS = 0, CPLD_RESULT_MEM_ALLOC_FAILED, CPLD_RESULT_LIB_INITIALIZED, CPLD_RESULT_LIB_UNINITIALIZED, CPLD_RESULT_LIB_INITIALIZE_FAILED, CPLD_RESULT_LIB_UNINITIALIZE_FAILED, CPLD_RESULT_OPENED_NUMBER_OVER, CPLD_RESULT_OPEN_FAILED, CPLD_RESULT_INVALID_CERTIFICATE, CPLD_RESULT_INVALID_PARAMETER, CPLD_RESULT_VIDEO_SOURCE_GET_FAILED, CPLD_RESULT_VIDEO_SOURCE_SET_FAILED, CPLD_RESULT_VIDEO_RESOLUTION_NO_OUTPUT, CPLD_RESULT_VIDEO_RESOLUTION_NO_HDMI, CPLD_RESULT_VIDEO_RESOLUTION_INVALID, CPLD_RESULT_VIDEO_RESOLUTION_GET_FAILED, CPLD_RESULT_VERSION_8051_GET_FAILED, CPLD_RESULT_VERSION_CPLD_GET_FAILED, CPLD_RESULT_VERSION_FPGA_GET_FAILED

};

CPLDManGetVersion(int* ot_ipVerYear,int* ot_ipVerMonth, int* ot_ipVerDay)
 Description:

Get CPLDMan.dll version.

Parameter:

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ot_ipVerYear: Integer pointer of year version.

ot_ipVerMonth: Integer pointer of month version.

ot_ipVerDay: Integer pointer of day version.

Return:

An integer, see enum type.

2. CPLDManInitialize();

Description: Initialize CPLD library. Parameter: N/A. Return: An integer, see enum type.

3. CPLDManUninitialize();

Description: Uninitialize CPLD library. Parameter: N/A. Return: An integer, see enum type.

- 4. CPLDManOpen(int in_iBusNo)
 Description:
 Open CPLD.
 Parameter:
 in_iBusNo: Bus No. of CPLD.
 Return:
 An integer, see enum type.
- 5. CPLDManClose(int in_iBusNo)

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Description: Close CPLD. Parameter: in_iBusNo: Bus No. of CPLD. Return: An integer, see enum type.

 CPLDManCodecVideoSrcGet(int in_iBusNo, int in_iCodecNo, int* ot_ipValue) Description:

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Get video source of codec.

Parameter:

iBusNo: Bus No. of CPLD.

in_iCodecNo: Codec No.

ot_ipValue: Integer pointer of video source, used in get funcion.

Return:

An integer, see enum type.

CPLDManCodecVideoSrcSet(int in_iBusNo, int in_iCodecNo, int in_iValue)
 Description:
 Set video source of codec.
 Parameter:

iBusNo: Bus No. of CPLD. in_ iCodecNo: Codec No. in_iValue: Video source, used in set function. Return:

An integer, see enum type.

CPLDManTXVideoSrcGet(int in_iBusNo, int in_iTXNo, int* ot_ipValue)
 Description:

Get video source of transmitter.

Parameter:

iBusNo: Bus No. of CPLD.

in_ iTXNo: Transmitter No.

ot_ipValue: Integer pointer of video source, used in get funcion.

Return:





An integer, see enum type.

9. CPLDManTXVideoSrcSet(int in_iBusNo, int in_iTXNo, int in_iValue)
Description:
Set video source of transmitter.
Parameter:
iBusNo: Bus No. of CPLD.

in_ iTXNo: Transmitter No. in_iValue: Video source, used in set function. Return: An integer, see enum type.

- 10. CPLDMan8051Version(int in_iBusNo, int* ot_ipValue);
- 11. CPLDManCPLDVersion(int in_iBusNo, int* ot_ipValue);
- CPLDManFPGAVersion(int in_iBusNo, int* ot_ipValue);
 Description:

Get firmware version of 8051 / CPLD / FPGA.

Parameter:

in_iBusNo: Bus No. of CPLD.

ot_ipValue: Integer pointer of firmware version.

Return:

An integer, see enum type.

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3.2.3 Mb86H55rebDII API Introduction

The Mb86H55rebDll API only has C# version now. The detail usage can be found in the source code.

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3.2.4 Role of Mb86H55rebDll API



The application can use Mb86H55rebDll API to control capture card.

3.2.5 Using Mb86H55rebDII API

Step 1: Put the "ApCmn.dll", "ApScenario.dll", "DumpFile.dll", "Mb86H55rebDll.dll" and "PushFileSource2.dll" in the folder where execution file exist.



The DumpFile.dll and PushFileSource2.dll are filters of DirectShow. The user must register them before using them, otherwise an error will occur.

Step 2: Use name space:

using Mb86H55rebDll;



```
Step 3:
         Declare variable to control MB86H55 as below:
         Mb86H55reb mb86h55reb = new Mb86H55reb;
Step 4: Add the following event handler:
         protected override void WndProc(ref Message m)
         {
            DoMb86h55Events(ref m);
            base.WndProc(ref m);
        }
         private void DoMb86h55Events(ref Message m)
         {
            Mb86H55reb.AsyncEventResult result;
            string comment;
            result = mb86h55reb.OnMsg(ref m, out comment);
            UpdateScreenAfterEvents(result, comment);
        }
Step 5: In the function UpdateScreenAfterEvents(),other control functions can be added
         according to the purpose. For example: Error message report function.
         private void UpdateScreenAfterEvents(Mb86H55reb.AsyncEventResult result,
         string comment)
         {
            switch (result)
            {
            case Mb86H55reb.AsyncEventResult.OperationComplete:
                 break:
            case Mb86H55reb.AsyncEventResult.OperationCompleteStop:
                 mb86h55reb.Reset();
                 break;
            case Mb86H55reb.AsyncEventResult.OperationCompleteAutoStop:
```

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```
mb86h55reb.Reset();
        break;
   case Mb86H55reb.AsyncEventResult.OperationCancel:
        break;
   case Mb86H55reb.AsyncEventResult.Warning:
        break;
   case Mb86H55reb.AsyncEventResult.SeriousError:
        break:
   case Mb86H55reb.AsyncEventResult.HdmiCableStatusChanged:
        break;
   case Mb86H55reb.AsyncEventResult.OperationContinue:
        break;
   case Mb86H55reb.AsyncEventResult.AudioStatusChanged:
        break;
   default:
        break;
   }
}
void SystemEvents_PowerModeChanged(object sender,
Microsoft.Win32.PowerModeChangedEventArgs e)
{
switch (e.Mode)
   {
case Microsoft.Win32.PowerModes.Suspend:
        mb86h55reb.Close();
        break;
case Microsoft.Win32.PowerModes.Resume:
mb86h55reb.DirectShowEnabled(miChipNo, mbDirectShowEnabled);
mblsMb86h55rebOpened = mb86h55reb.Open(miChipNo,this.Handle);
```

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mb86h55reb.SetCanvasHandle(mPnlCanvas.Handle); mb86h55reb.ApplyGpio(); mb86h55reb.RebootFirm(); SetScreenMode(ScreenMode.Processing); mb86h55reb.Reset(); break; }

Step 6: Before using MB86H55REB, it must be initialized:

mb86h55reb.Close();

mb86h55reb.DirectShowEnabled(miChipNo, mbDirectShowEnabled);

mblsMb86h55rebOpened = mb86h55reb.Open(miChipNo,this.Handle);

mb86h55reb.SetCanvasHandle(mPnlCanvas.Handle);

mb86h55reb.ApplyGpio();

mb86h55reb.RebootFirm();

mb86h55reb.Reset();

Step 7: Refer the following function for detail:

frmMain_Load()

SystemEvents_PowerModeChanged()

cmbBoardSelection_SelectedIndexChanged()

3.2.6 Mb86H55rebDII API Description

Simplify description of Mb86H55rebDll variable, interface and API. Refer to the source code to get the detail usage.

Variable:

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1. string h264FileName

Encode / decode file name.

Interface

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1. FMBVideoFormatEnum h264VideoFormat Video formate. RTechnology Corp.

enum FMBVideoFormatEnum

{

FMBEnmVideoFmt1920x1080, FMBEnmVideoFmt1440x1080,

> FMBEnmVideoFmt1280x720, FMBEnmVideoFmt720x480, FMBEnmVideoFmt720x576, EnmVideoNumofFmt

};

2. FMBVideoFrameEnum h264VideoFrame Video frame rate.

enum FMBVideoFrameEnum

{

FMBEnmVideoFrm_60p,

FMBEnmVideoFrm_5994p,

- FMBEnmVideoFrm_50p,
- FMBEnmVideoFrm_60i,

FMBEnmVideoFrm_5994i,

FMBEnmVideoFrm_50i,

EnmVideoNumofFrm

};

3. FMBVideoRateCtlEnum h264VideoRateCtl

Video rate control.

enum FMBVideoRateCtlEnum

{ FMBEnmVideoRateCtlCbr, FMBEnmVideoRateCtlVbr,







- 4. int h264VideoBitrateCbr Video CBR bitrate value.
- 5. int h264VideoBitrateAverage Video average bitrate for VBR.
- 6. int h264VideoBitratePeak Video peak bitrate for VBR.
- 7. int[] h264Pids = new int[(int)PidTypeEnum.EnmPidNumofPid]; PID value array.

enum PidTypeEnum

{

EnmPidVideo, EnmPidAudio, EnmPidPmt, EnmPidSit, EnmPidPcr, EnmPidNumofPid

};

8. FMBFuncModeEnum operationMode

Operation mode.

enum FMBFuncModeEnum

{

 ${\sf FMBEnmFuncModeEnc},$

FMBEnmFuncModeDec,

};

9. int pciNo

Get current PCI / chip No.

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 bool isStreamRunning Get is stream runnging.

API

bool Open(int pciNoArg, IntPtr hWnd)
 Description:
 Open device.
 Parameter:
 pciNoArg: Device (chip) No.
 hWnd: Window handle.

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2. void Close()

Description: Close device.

- 3. void Encode() Description: The encode is begun.
- 4. void Decode() Description: The decode is begun.
- 5. void Stop() Description: The stop is begun.
- 6. void Reset()Description:The reset is begun.
- 7. AsyncEventResult OnMsg(ref Message m, out string comment)

Description:

It is processed to receive the message.



Parameter:

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m: Value of message comment: Comment form me Return: Value of AsyncEventResult

public enum AsyncEventResult

{

UnknownEvent, OperationContinue, OperationComplete, OperationCompleteStop, OperationCompleteAutoStop, OperationCancel, Warning, SeriousError, HdmiCableStatusChanged,

AudioStatusChanged,

}

8. bool Equals(ref Mb86H55reb target)

Description:

Oneself is compared with the argument.

Parameter:

target: target

Return:

true:equal, false:not equal.

9. void CommitProperty()

Description:

The change in property is committed.

10. void ApplyGpio()

Description:

Property is applied to the GPIO device.



11. void RebootFirm() Description: Firm is rebooted.

12. void SetChipNo(int in_iChipNo)

Description:

Set device (chip) No.

This function will change the chip ID, use it be carefully.

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Parameter:

in_iChipID: Chip ID.

in_iBusNumber: Bus No.

in_iDevNumber: Device No.

13. void DirectShowEnabled(int in_iChipNo, bool in_bFlag)

Description:

Enable / disable DirectShow.

Parameter:

in_iChipNo: Chip No.

in_bFlag: true is enabled, false is disabled.





- 3.3 DirectShow Graph
 - 3.3.1 Encoding Graph





3.4 Architecture of SDK

Chip / codec / device usually means the same thing.

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FAQ



Q: Capture card, driver, application and input source are ready, but the recorded video is not displayed or displayed incorrectly.

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A: A correct video codec is needed to display the H.264 video image. For example: ffdshow codec.

Q: How do I check the current DirectX version?

A: In Windows, navigate to Start \rightarrow Run \rightarrow Type 'dxdiag' \rightarrow Enter. The current version is displayed in the DirectX Diagnostic Tool window.

Q: Input source and encoding are both set to 1080 60p, but the application cannot encode.

A: The chip is critical for input stream timing. Make sure the input stream frequency is 1080 60p.

Q: How do I obtain the latest driver?

A: Go to <u>http://www.ieiworld.com/</u>. You can always find and download the latest drivers from the "Support" pages.







Error Code



A.1 Error Code Overview

Error register (M_ERROR_INFO_H and M_ERROR_INFO_L)

Name	M_ERROR_INFO_H		M_ERROR_INFO_L
Bit	15		150
Field	ERROR_		
FIEID	MODULE[7:0]	ERROR_STATUS[23:0]	

A.2 ERROR_MODULE[7:0]

Module where the error occurred.

The table below outlines the relationship between values and modules.

Value	Module Name	Function	Mode
0x00	HOSTCMD	Host communication library	ENC, DEC
0x01	ETOP	Recorder-wide controller	ENC
0x02	DTOP	Player-wide controller	DEC
0x03	BACKGROUND	Interrupt controller	ENC, DEC
0x04	VREC	Video input controller	ENC
0x05	VMUX	Video multiplex controller	ENC
0x06	AREC	Audio input controller	ENC
0x07	AMUX	Audio multiplex controller	ENC
0x08	SMUX	System multiplex controller	ENC
0x09	VPLAY	Video playback controller	DEC
0x0A	VDMX	Video decode controller	DEC
0x0B	APLAY	Audio playback controller	DEC
0x0C	ADMX	Audio decode controller	DEC
0x0D	SDMX	System stream controller	DEC
0x0E	SAPI	Serial communication controller	ENC, DEC

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A.3 ERROR_STATUS[23:0]

Detailed error cause. The relationship between the values and error causes is described in the following sections.

A.3.1 IDLE

ERROR_	MODULE[7:0]	ERROR_MODULE[23:0]		Outline	
Value	Name	Value	Name	Outline	
		0,000,0001		The system command parameter	
	0x00_000	0x00_0001		cmd_id is invalid	
0x00 HOSTCMD	0x00_0002	SCMD_CC_NOT_CONTINUOUS	The system command parameter		
			continuity_counter values are not		
			consecutive		
		HOSTCMD_ERR_SCMD_			
	0x00_0003		UNACCEPTABLE_FIRMWARE	Incorrect IIrmware	



A.3.2 ENC

ERROR_MODULE[7:0]		ERROR_MODULE[23:0]		Outline	
Value	Name	Value	Name	Outline	
		0,000,0001		The system command	
		0000_0001		parameter cmd_id is invalid	
				The system command	
0x00	HOSTCMD	0x00_0002	SCMD_CC_NOT_CONTINUOUS	parameter continuity_counter	
				values are not consecutive	
		0x00 0003	HOSTCMD_ERR_SCMD_	Incorrect firmware	
		0x00_0003	UNACCEPTABLE_FIRMWARE	Theoreet firmware	
0x01	ETOP	0x00_0001	FIFO_OVERFLOW	The event queue overflowed	
				An invalid value is specified in	
		0x00_0002	2 INVALID_SCMD_INIT_PARAM	initialization dedicated	
				parameter register M	
				An invalid value is specified in	
		0x00_0003	INVALID_VCMD_INIT_PARAM	initialization dedicated	
				parameter register V	
		0x00_0004	INVALID_ACMD_INIT_PARAM	An invalid value is specified in	
				initialization dedicated	
				parameter register A	
		0x00 0005	LINACCEPTABLE EVENT	An event that cannot be handled	
		0,000_00000		by the ETOP was received	
		0x00_0006		The system command	
		0,000_0000		parameter cmd_id is invalid	
			ANOTHER SCMD	A subsequent system command	
		0x00_0007	BEFORE SCMD ACK	was received before an	
			BEI OKE_SCIID_ACK	acknowledge was returned	
			SCMD CC NOT	The system command	
		0x00_0008		parameter continuity_counter	
				values are not consecutive	
		0x00 0009		The system command	
		0x00_0009	INVALID_SIMES_ACK_CMID_ID	parameter cmd_id is invalid	



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		0x00_000A 0x00_000B 0x00_000C 0x00_000D	ANOTHER_SMES_ACK_ BEFORE_SMES SMES_ACK_CC_ NOT_CONTINUOUS SMES_ACK_NOT_RECIEVED INNER_ERROR	An acknowledge was received althought no subsequent system message was sent The system command parameter continuity_counter values are not consecutive Before reception of a system message acknowledge, the next message was generated ETOP internal error
		0x00_000E	EVENT_QUEUE_OVERFLOW	The event queue overflowed
0x03	BACKGROUND			
		<u> </u>	<u> </u>	
		0x00_0001	INVALID_VCMD_INIT_PARAM	The value of the initialization dedicated parameter register V is invalid
		0x00_0002	VIDEO_CPU_ACCESS	There is a problem concerning communication with the video section
		0x00_0003	FIFO_OVERFLOW	Some idx_fifo overflowed
0x04	VREC	0x00_0004	FIFO_EMPTY	Some idx_fifo became empty
		0x00_0005	UNACCEPTABLE_ HOSTCMD_EVENT	Invalid HOSTCMD event
		0x00_0006	UNACCEPTABLE_EVENT	Invalid event
		0x00_0007	INVALID_IDX	The VRAW _idx value became invalid
		0x00_0008	INNER_ERROR	VREC internal error
		0x00_0009	EVENT_QUEUE_OVERFLOW	The event queue overflowed
	r	1	Γ	
0x05	VMUX	0x00_0001	NG	Unclassified VMUX internal error
		0x00_0002	UNDERFLOW	A video stream buffer underflow was detected
		0x00_0003	VBV_BOC	VBV discontinuity was detected

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		0,000,000,4		A stream buffer overwrite was
		0x00_0004	STRM_BUF_OVERWRITTEN	detected
		0x00_0005	INVALID_HOST_CMD	Invalid HOSTCMD event
		0x00_0006	INVALID_EVENT	Invalid event
		0x00_0007	FIFO_OVERFLOW	Some idx_fifo overflowed
		0x00_0008	FIFO_EMPTY	Some idx_fifo became empty
		0,000,0000		Invalid command to the
		0X00_0009		multiplexing section
		A000_00x0	INVALID_PARAM	Invalid parameter
		0,000 0001		Invalid initialization parameter
		0x00_0001	INVALID_INIT_PARAM	value
				There is a problem concerning
		0x00_0002	AUDIO_CPU_ACCESS	communication with the audio
				section
		0x00_0003	AUDI_IN	An error occurred during audio
				input access
0206	ADEC	0x00_0004	FIFO_OVERFLOW	Some idx_fifo overflowed
0,00	AREC	0x00_0005	FIFO_EMPTY	Some idx_fifo became empty
		0x00_0006	UNACCEPTABLE_	Invalid HOSTCMD event
			HOSTCMD_EVENT	
		0x00_0007	UNACCEPTABLE_EVENT	Invalid event
		0x00_0008	INVALID_IDX	The ARAW_idx value became
		0,000_0000		invalid
		0x00_0009	INNER_ERROR	AREC internal error
		0x00_000A	EVENT_QUEUE_OVERFLOW	The event queue overflowed
		0x00_0001	NG	Unclassified AMUX internal error
		0x00_0002	INVALID_HOST_CMD	Invalid HOSTCMD event
		0x00_0003	INVALID_EVENT	Invalid event
0x07	AMUX	0x00_0004	FIFO_OVERFLOW	Some idx_fifo overflowed
		0x00_0005	FIFO_EMTPY	Some idx_fifo became empty
		0x00_0006	INVALID_MUXCMD	Invalid command to the multiplexing section



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-	- 65		HI	JC-JUZE JUK (WINDO)
		0x00_0001	NG	Unclassified SUX internal e
		0x00_0002	INVALID_HOST_CMD	Invalid HOSTCMD event
		0x00_0003	INVALID_EVENT	Invalid event
		0x00_0004	INVALID_EVENT_SOURCE	Invalid event issuer
		0x00_0005	INVALID_EVENT_PARAM	Invalid event parameter
		0x00_0006	INVALID_PARAM	Invalid parameter
		0x00_0007	START_STC	STC start processing error
		0x00_0008	FIRST_PCR	FIRST_PCR processing err
0x08	SMUX	0x00_0009	FIRST_PAT	FIRST_PAT processing error
	0.000	INVALID_	VMUX or AMUX state trans	
		0x00_000A	VMUX_AMUX_STATE	error
		0x00_000B	AUTO_NULL_ON	NULL output processing er
		0×00,0000		NILL output stop processir
		0x00_000C	AUTO_NULL_OFF	error
		0x00_000D	STOP	Stop processing error
		0,000,0005		Invalid command to the
		0X00_000E		multiplexing section
		0x00_0001	OVERFLOW	A buffer overflow occurred
OVOE	SADI			A data transmission compl
UXUL	SAL	0x00_0002	UNEXPECTED_	interrupt was received whe
			TRANS_DATA_IRQ	should not have been
				Error notification from the
		0x00_0001		section (details are display
		0,00_0001		the error register
OxFF	(Special,			V_ERROR_INFO)
	tentative)			Error notification from the
		0x00 0002		section (details are display
		0,00_0002		the error register
				A_ERROR_INFO)

A.3.3 DEC

ERROR_MODULE[7:0]		ERROR_MODULE[23:0]		Outline	
Value	Name	Value	Name	Outime	
		000.0001		The system command parameter	
		0x00_0001	INVALID_SCMD_CMD_ID	cmd_id is invalid	
				The system command parameter	
0x00	HOSTCMD	0x00_0002	SCMD_CC_NOT_CONTINUOUS	continuity_counter values are not	
				consecutive	
		0,000,0003	HOSTCMD_ERR_SCMD_	Incorroct firmwara	
		0x00_0003	UNACCEPTABLE_FIRMWARE	Incorrect In mware	
0x02	DTOP	0x00_0001	FIFO_OVERFLOW	An FIFO overflow occurred	
				The value specified in the	
		0x00_0002	INVALID_SCMD_INIT_PARAM	initialization dedicated parameter	
				register M is invalid	
				The value specified in the	
		0x00_0003	INVALID_VCMD_INIT_PARAM	initialization dedicated parameter	
				register V is invalid	
			x00_0004 INVALID_ACMD_INIT_PARAM	The value specified in the	
		0x00_0004		initialization dedicated parameter	
				register A is invalid	
		0x00_0005	UNACCEPTABLE_	An invalid stream input control	
		0000_0000	STRM_INPUT_EVENT	event was received	
		0x00_0006	UNACCEPTABLE_	An invalid state transition	
		0000_0000	STATE_CHANGE_EVENT	notification event was received	
		0x00_0007	INVALID SCMD CMD ID	The system command parameter	
				cmd_id is invalid	
		0x00_0008	INVALID SCMD SUB CMD ID	The system command parameter	
				sub_cmd_id is invalid	
				A subsequent system command	
		0x00 0009	ANOTHER_SCMD_	was received before an	
			BEFORE_SCMD_ACK	acknowledge was returned	



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		0x00_000A	SCMD_CC_NOT_CONTINUOUS	The system command parameter continuity_counter values are not consecutive
		0x00_000B	INVALID_VIDEO_CPU_STATE	The state of the video section is invalid
		0x00_000C	INVALID_AUDIO_CPU_STATE	The state of the audio section is invalid
		0x00_000D	ERROR_NOTIFIED_ FROM_VIDEO_CPU	Error notification from the video section (details are displayed in the error register V_ERROR_INFO)
		0x00_000E	ERROR_NOTIFIED_ FROM_AUDIO_CPU	Error notification from the audio section (details are displayed in the error register A_ERROR_INFO)
		0x00_000F	INNER_ERROR	DTOP internal error
		0x04_xxxx		An error occurred during
				processing of an interrupt from
				the AUDIO_SPDIF output
		0x05_xxxx 0x09_xxxx		An error occurred during
				processing of an interrupt from
0x03	BACKGROUND			the audio output
				An error occurred during
				processing of an interrupt from
				the video output
				An error occurred during
		0x0D_xxxx		processing of an interrupt from
				the stream splitter
0x09	VPLAY	0x00_0001	FIFO_OVERFLOW	An FIFO overflow occurred
	0x00	0x00_0002	UNACCEPTABLE_	An invalid state transition
			HOSTCMD_EVENT	instruction event was received
		0x00_0003	INVALID_PARAM	Invalid argument

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0x00_0001

APLAY

0x0B

FIFO_OVERFLOW

				The initialization dedicated
		0x00_0004	INVALID_INIT_PARAM	parameter register V is invalid
		0x00 0005	INVALID VIDEO OUT STATE	The state of the video output
				hardware is invalid
		0x00 0006	INVALID AUDIO OUT STATE	The state of the audio output
				hardware is invalid
		0x00 0007	INVALID_	The STC state of the video output
			VIDEO_OUT_STC_STATE	hardware is invalid
		0x00_0008	INVALID	The STC state of the audio output
		0x00_0000	_AUDIO_OUT_STC+STATE	hardware is invalid
		0x00_0009	INNER_ERROR	VPLAY module internal error
		0x00 0004		The internal state of the video
		0,000_0004	VIDOL_OUT_INNER_ERROR	output hardware is invalid
		0x00_000B		The internal state of the audio
		0,000_0000		output hardware is invalid
		0×00,0000		No corrective measure has been
		0x00_0000		implemented
		0x00_000D	NO_VALID_VIDEO_ES_INPUT	An urgent action was take because
				a valid video ES input delay was
				detected
		0x00_0001	FIFO_OVERFLOW	An FIFO overflow occurred
		0x00_0002	INVALID_PARAM	Invalid argument
		0,000,0003	UNACCEPTABLE_	An invalid HOSTCMD event was
	0x0A VDMX	0x00_0003	HOSTCMD_EVENT	received
0x0A		0.000 0004		The state of the video section is
	0x00_0004	INVALID_VIDEO_CPU_STATE	invalid	
		0x00_0005	INNER_ERROR	VDMX module internal error
		0,000,000(Stream splitting hardware internal
		0x00_0006		error
				An FIFO overflow occurred

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			The initialization dedicated
	0x00_0002	INVALID_INIT_PARAM	parameter register A is invalid
	0x00_0003	UNACCEPTABLE_	Invalid HOSTCMD event
		HOSTCMD_EVENT	
	0x00_0004	ΙΙΝΑΟΟΕΡΤΑΒΙ Ε	Invalid AUDIO_
			OUTPUT_
		AUDIO_UUI_CONTROL_EVENT	CONTROL event
	0.000 0005	UNACCEPTABLE_	Invalid AUDIO_
	0x00_0005	AUDIO_OUT_DONE_EVNET	OUTPUT_DONE event
			The state of the audio output
	0x00_0006	INVALID_AUDIO_OUT_STATE	hardware is invalid
		INVALID_	The STC state of the audio output
	0x00_0007	AUDIO_OUT_STC_STATE	hardware is invalid
	8000_00x0	INVALID_MUTE_CONTROL	Invalid mute control was used
			The specification of the next frame
	0x00_0009	NEXT_AFRAME_IDX_CONFLICT	was repeated
			The internal state of the APLAY
	0x00_000A	INNER_ERROR	module is invalid
	0x00_000B		The internal state of the audio
		AUDIO_OUT_INNER_ERROR	output hardware is invalid
		UNIMPLEMENTED	
	0x00_000C		No corrective measure has been
	0x00_000D	INVALID_	The state of the audio output
		AUDIO_OUT_SPDFI_STATE	hardware (SPDIF) is invalid
	0x00_000E	AUDIO_OUT_	The internal state of the audio
		SPDIF_INNER_ERROR	output hardware (SPDIF) is invalid
ADMX	0x00_0001	FIFO_OVERFLOW	An FIFO overflow occurred
	0x00_0002	INVALID_PARAM	Invalid argument
	0×00 0003	UNACCEPTABLE_	An invalid HOSTCMD event was
	0.00_0003	HOSTCMD_VENT	received
	0.000.0001		The state of the audio section is
	0x00_0004	INVALID_AUDIO_CPU_STATE	invalid
	ADMX	0x00_0002 0x00_0003 0x00_0004 0x00_0005 0x00_0006 0x00_0007 0x00_0008 0x00_0008	ADMX0x00_0002INVALID_INIT_PARAM0x00_0003UNACCEPTABLE_ HOSTCMD_EVENT0x00_0004UNACCEPTABLE_ AUDIO_OUT_CONTROL_EVENT0x00_0005UNACCEPTABLE_ AUDIO_OUT_DONE_EVNET0x00_0006INVALID_AUDIO_OUT_STATE0x00_0007INVALID_ AUDIO_OUT_STC_STATE0x00_0008INVALID_MUTE_CONTROL0x00_0009NEXT_AFRAME_IDX_CONFLICT0x00_00004INNER_ERROR0x00_00004INNER_ERROR0x00_00005INVALID_ AUDIO_OUT_STC_STATE0x00_00004INNER_ERROR0x00_00005INVALID_ AUDIO_OUT_SPDFI_STATE0x00_00005INVALID_ AUDIO_OUT_SPDFI_STATE0x00_0001FIFO_OVERFLOW0x00_0002INVALID_PARAM0x00_0003INVALID_PARAM0x00_0004INVALID_PARAM0x00_0003INVALID_PARAM0x00_0004INVALID_PARAM0x00_0003INVALID_PARAM0x00_0004INVALID_PARAM0x00_0005INVALID_VENT

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		0x00_0005	INNER_ERROR	VDMX module internal error
		0x00_0006	DEMUX_INNER_ERROR	Stream splitting hardware internal error
OxOD	SDMX	0x00_0001	FIFO_OVERFLOW	An FIFO overflow occurred
		0x00_0002	INVALID_PARAM	Invalid argument
		0x00_0003	INVALID_INIT_PARAM	The initialization dedicated
				parameter register M is invalid
		0x00_0004	INVALID_DEMUX_STATE	The state of the stream splitting
				hardware is invalid
		0x00_0005	SEQ_NUM_UNCNAHGED	seq_num is the same as the value
				previously specified
		0x00_0006	UNACCEPTABLE_	An invalid HOSTCMD event was
			HOSTCMD_EVENT	received
		0x00_0007	INNER_ERROR	SDMX module internal state error
		0x00_0008	UNACCEPTABLE_STRM_ INPUT_CONTROL_EVENT	An invalid STRM_
				INPUT_CONTROL event was
				received
		0x00_0009	DEMUX_INNER_ERROR	The internal state of the stream
				splitting hardware is invalid
		0x00_000A	INVALID_SECTION_FROMAT	The PSI section is in an invalid
				format
OxOE	SAPI	0x00_0001	OVERFLOW	A buffer overflow occurred
		0x00_0002		A data transmission completion
			TRANS_DATA_IRQ	interrupt was received when it
				should not have been

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